

LT-840-700 4CH CC DMX512 DECODER



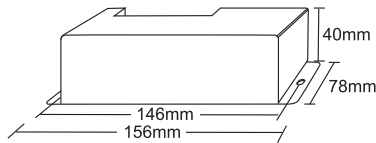
LT-840-700 is a 4CH CC DMX512 decoder, output 700mA CC PWM signal per channel, control hi-power LED lamps without CC driving IC (e.g.: LED downlights) or with the current-limiting resistor (e.g.: LED strips) and the low power LED lamps (e.g.: LED panel lights). The DMX decoder works with DMX512 console, 256 grayscale output per channel, 0-100% dimming range with various changing effects. Equipped with DMX standard XLR-3, RJ45 and green terminal interface, control single color, bi-color, RGB and RGBW/Y LED lights.

1. Product Parameter:

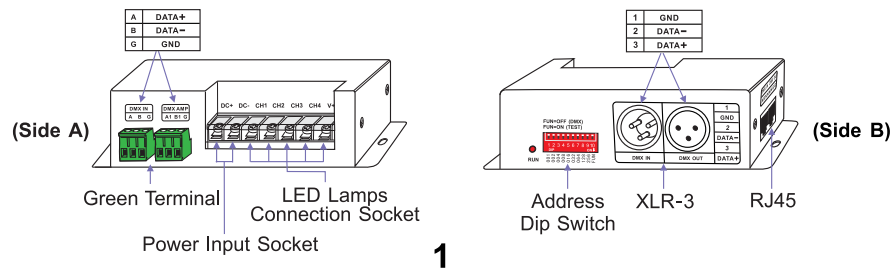
LT-840-700

- Input Voltage: 12~48VDC
- Output Voltage: 3~42VDC
- Output Current: CC 700mA×4CH
- Output Power: 2.1W~29.4W×4CH Max 117.6W
- Driving LEDs: Series 1~12pcs 3W LEDs×4CH
- DMX512 Socket: XLR-3, RJ45, Green Terminal
- Input Signal: DMX512
- Dimming Range: 0~100%
- Working Temperature: -30°C~65°C
- Dimensions: L156×W78×H40(mm)
- Package Size: L180×W82×H48(mm)
- Weight (G.W.): 445g

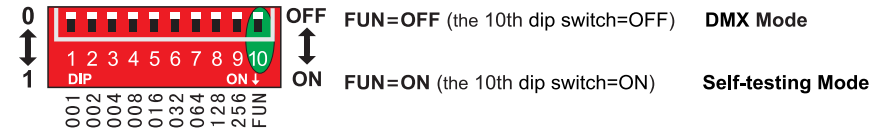
2. Product Size:



3. Configuration Diagram:



4. Dip Switch Operation:

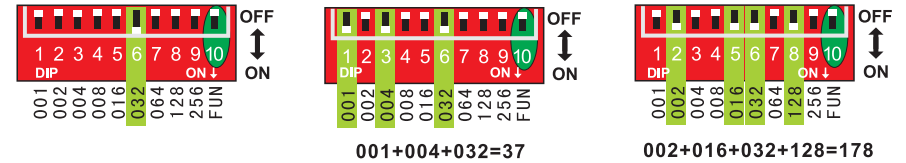


4.1 DMX Mode:

FUN=OFF (the 10th dip switch=OFF) **DMX Mode**

DMX address value=the total value of (1-9), to get the place value when in "on" position, otherwise will be 0.

E.g.1: Set Initial Address To 32. E.g.2: Set Initial Address To 37. E.g.3: Set Initial Address To 178.



* When Dip switch 1-9 are OFF, the defaulted initial DMX address is 1.

4.2 DMX Dimming Instruction:

Each LT-840-700 DMX decoder occupied 4 DMX addresses when connecting the DMX console, e.g. the defaulted initial DMX address is 1, please find their corresponding relationships in the following form:

DMX Console Channel	DMX Decoder Output Channel
CH1 0-255	CH1 PWM 0-100% (LED R)
CH2 0-255	CH2 PWM 0-100% (LED G)
CH3 0-255	CH3 PWM 0-100% (LED B)
CH4 0-255	CH4 PWM 0-100% (LED W/Y)

4.3 Manual Dimming Functions:

As figure, while FUN=OFF, disconnect the DMX512 signal, entering the manual dimming mode with the dip switch.

Brightness	DIP1-3(CH1)	DIP4-6(CH2)	DIP7-9(CH3)	Figure
0	000	000	000	
14%	100	100	100	
28%	010	010	010	
43%	110	110	110	
57%	001	001	001	
71%	101	101	101	
86%	011	011	011	
100%	111	111	111	